

Announcement

NUMONYX DEPLOYS Z CIRCUIT ZCHAR ALL-IN-ONE CELL LIBRARY CHARACTERIZATION

*Solution selected for corporate-wide IP collateral
characterization including standard cells, complex cells, and memory*

Mountain View, California, July 25, 2008 – Z Circuit, the leading provider of all-in-one cell library characterization tools, today announced Numonyx B.V. has selected the ZChar All-in-one cell library characterization system for corporate-wide IP collateral characterization including standard cells, complex cells, and memory.

“The main advantage for ZChar is the flexibility it provides in a single-system solution.” said Thomas Parker, R&D Manager For Basic Collateral at Numonyx. “With ZChar we can efficiently characterize simple cells and at the same time it can be extended to very complicated circuits”

Flexibility to Meet IP Requirements For SOC Design Flows

Companies producing next-generation integrated circuit designs have a need to produce accurate cell library models for their entire digital IP portfolio including standard cells, memories, and IO pads. In addition, due to manufacturing expenses and increasingly broad product requirements, many designs today are System-on-Chip (SOC) designs that are more complex and have a wider range of components than previous generation IC designs. Traditional cell library characterization tools are limited to a subset of these library components that result in companies having to use manual, error-prone and time-consuming methods to create the required cell models, often for their most complex cells.

These limitations, combined with an increase in recent years of cell model complexity as reflected by the more than 100x increase in the size of library model files, have led to many designers finding that their cell models and characterization systems are limiting their ability to accurately and efficiently complete their complex designs. Many existing cell library characterization systems have not been able to scale with the increasing data complexity demands required at the current process geometries.

Z Circuit’s **ZChar** cell library characterization and **Library Analyzer** validation tools are critical software systems specifically designed to scale with these increasing complexity demands.

Advanced Liberty Models for 40nm and below

ZChar automatically generates fully characterized cell models with the latest in state-dependent timing, power, noise, and statistical models for the customers entire set of library components. Z Circuit’s tools are fully integrated with all major EDA simulation

environments including those from Z Circuit's EDA partners: Synopsys, Cadence, and Mentor Graphics.

About Z Circuit

Z Circuit develops and markets Electronic Design Automation (EDA) software and provides a unique all-in-one solution for standard cell, memory, and IO pad cell library characterization and validation.

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